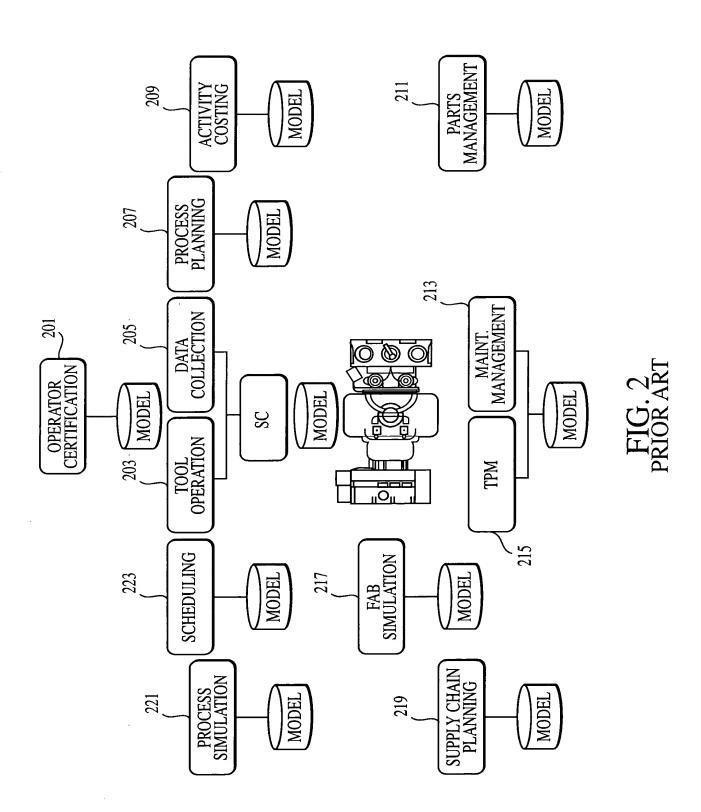


FIG. 1 PRIOR ART



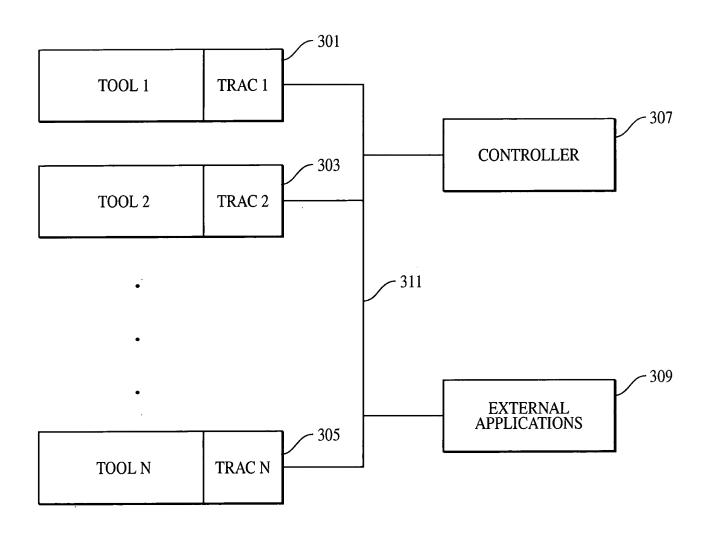


FIG. 3

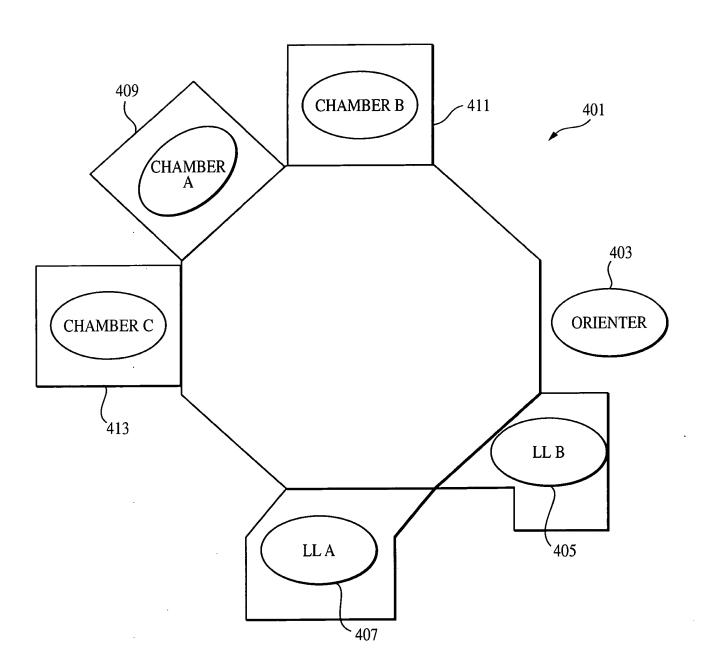


FIG. 4

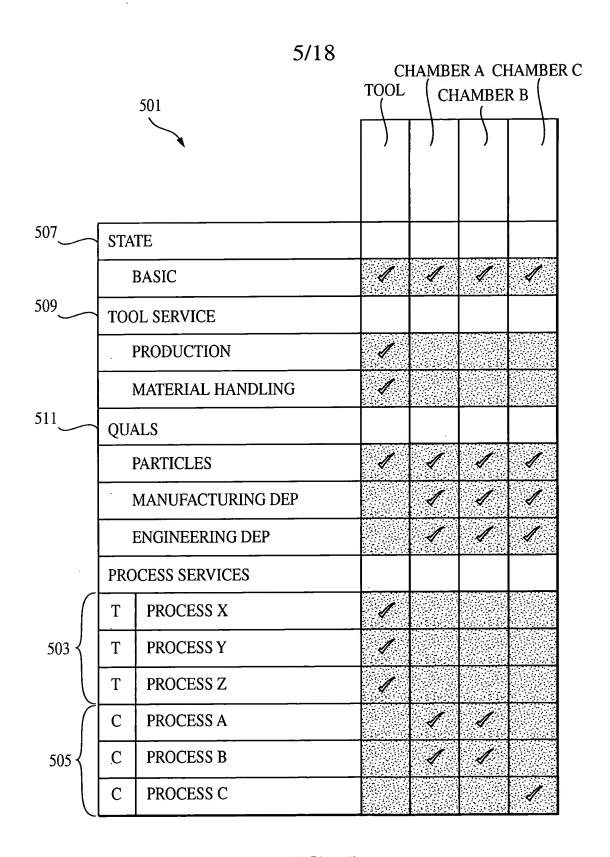


FIG. 5

	6/18	CHAMB TOOL (C		ER A CI	1
)))	
QoS - CpK					
PROCESS SERVICES					
PROCESS X					
PROCESS Y					
PROCESS Z					
PROCESS A			2.1	2.16	
PROCESS B			3.03	3.21	
PROCESS C					4.16

601

FIG. 6

	CHAMBER A CHAMBE TOOL (CHAMBER B (
QoS - PROC. TIME	
PROCESS SERVICES	
PROCESS X	
PROCESS Y	
PROCESS Z	
PROCESS A	42 43
PROCESS B	36 41
PROCESS C	130

FIG. 7

801

QoS - CpK	TOOL 1	TOOL 2	TOOL	TOOL 4
PROCESS SERVICES				
PROCESS X	2.35	2.1	2.16	×
PROCESS Y	3.41	3.03	3.21	×
PROCESS Z	×	4.19	4.21	4.16

FIG. 8

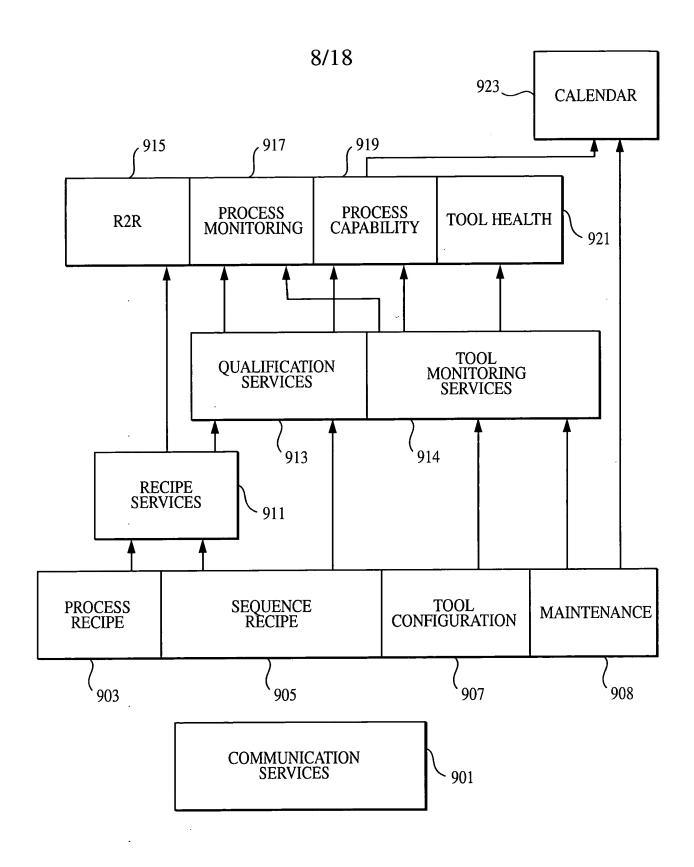
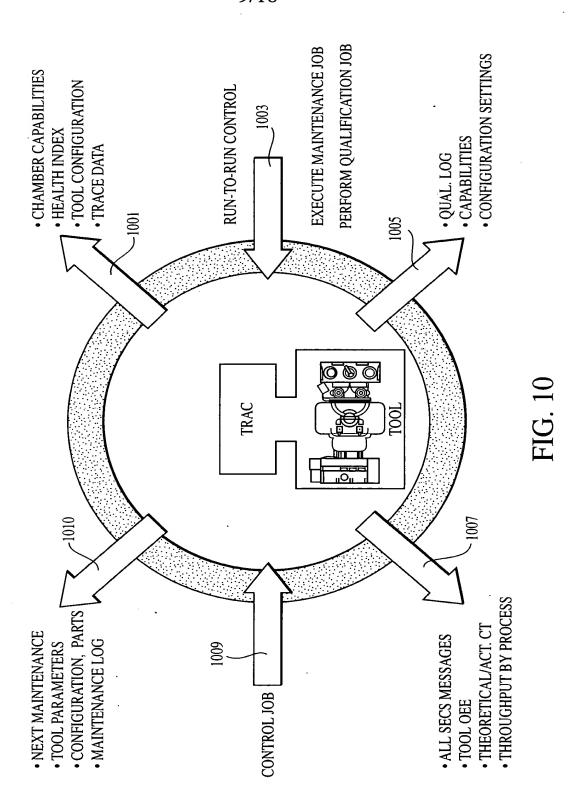


FIG. 9



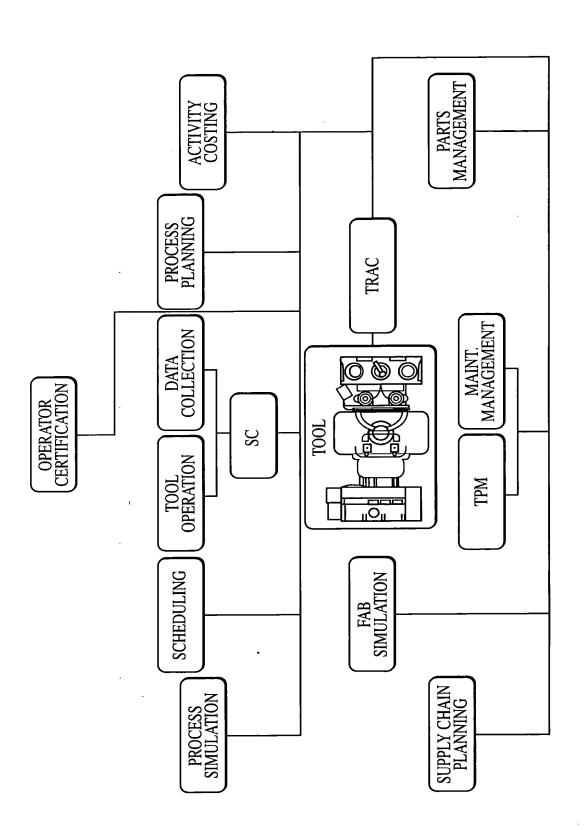


FIG. 11

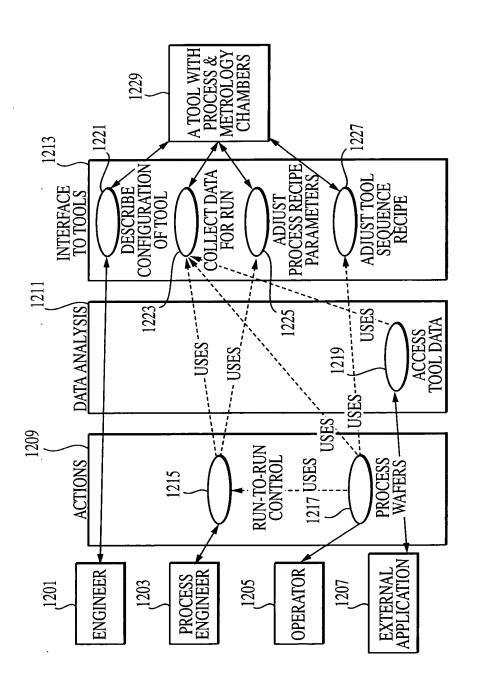


FIG. 12

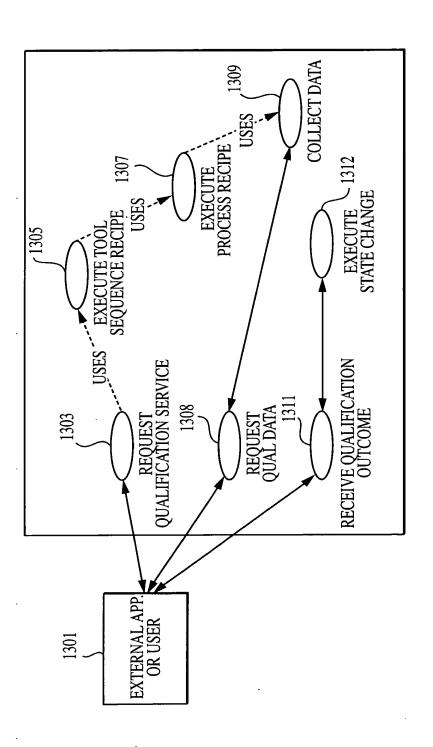


FIG. 13

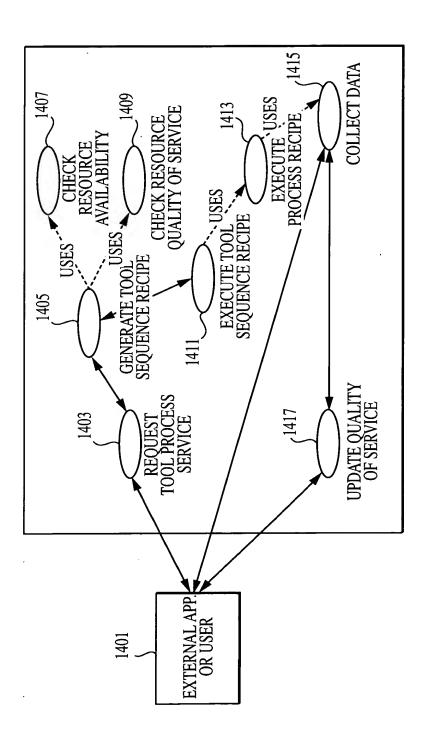


FIG. 14

1501	14/18	
EXTERNAL APPLICATION S2F41	<u>TRAC</u>	TOOL
PP SELECT S2F41 _ LOT START		THE SYSTEM INITIATES THE LOADING AND MAPPING OF THE WAFERS. AT COMPLETION THE SYSTEM SENDS
4	(R2R USES THIS AS LOT INFO EVENT)	S6F11 PROCESS START FOR PORT (LOT ID, SET OF WAFER ID'S, SET OF PROCESS RECIPES IN SEQUENCE RECIPE)
	CREATE EVENT REPORT(S). S2F35 LINK EVENT REPORT(S). S2F37 ENABLE EVENT REPORT(S). S2F49 ENABLES MODULE CONTRO FOR SPECIFIED CHAMBER(S)	DL 5).
	(R2R USES THIS AS SEND RECIPE EVENT) S2F49 RECIPE MODIFICATIONS	TX PROCESS DELAY TIMER IS INITIATED S6F11 WAFER DESTINATION REPORTED AS SOON AS THE TOOL KNOWS WHICH CHAMBER THE WAFER IS GOING TO.
◄	ARE SENT TO THE TOOL. (R2R USES THIS AS A RUN	IF TIMER EXPIRES ERROR HANDLING DEPENDENT ON TOOL CONFIGURATION S6F11 CHAMBER START EACH WAFER WILL GENERATE THIS EVENT EACH TIME A
	(R2R USES THIS AS SEND PLANNED INPUT)	WAFER ARRIVES AT A PROCESS CHAMBER. S6F11 PROCESS STEP COMPLETE SENDS AVERAGE PROCESS DATA FOR A SPECIFIC STEP.
	(R2R USES THIS AS A RUN STOP EVENT)	S6F11 PROCESS COMPLETE EACH WAFER WILL GENERATE THIS EVENT WHEN IT FINISHES PROCESSING IN EACH CHAMBER. S6F11 LOT COMPLETE
FIG. 15	(R2R USES THIS AS LOT DONE EVENT)	THIS EVENT REPORTS ONCE PER LOT, WHEN THE FINAL WAFER ARRIVES BACK IN THE LOAD LOCK.

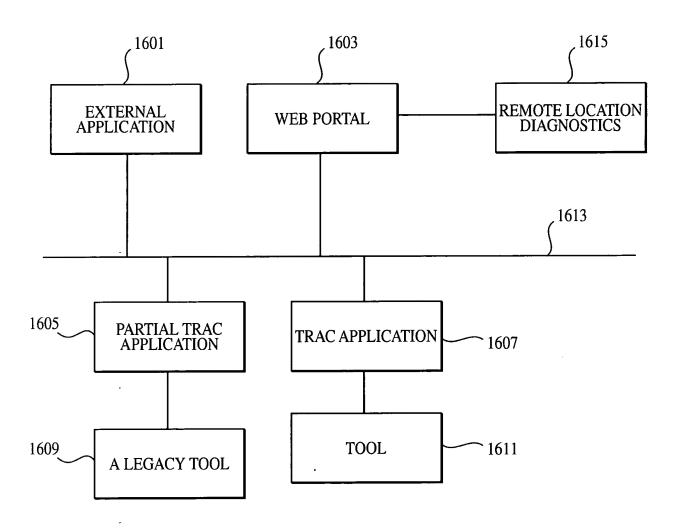


FIG. 16a

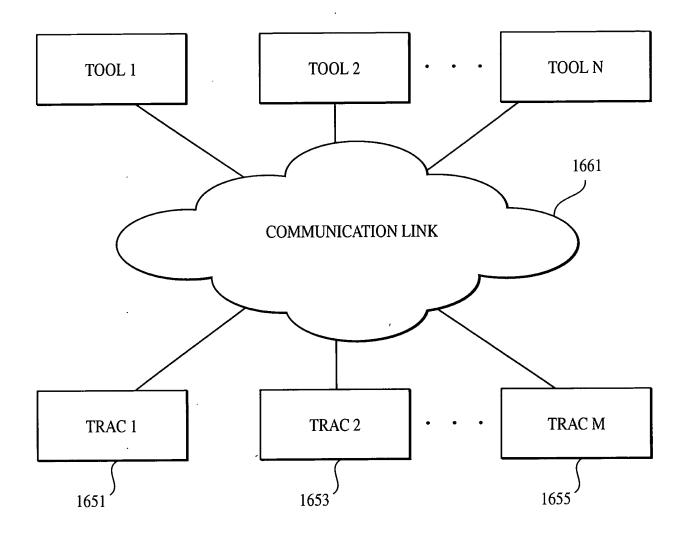


FIG. 16b

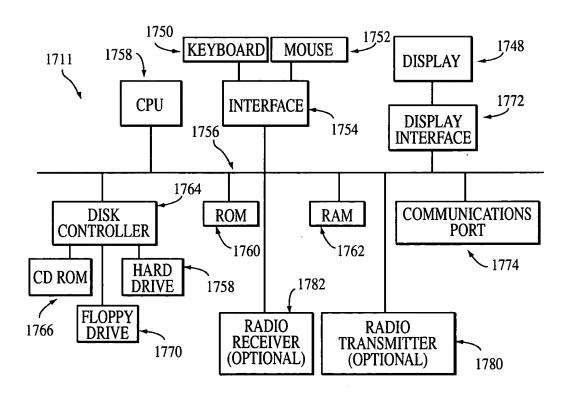


FIG. 17

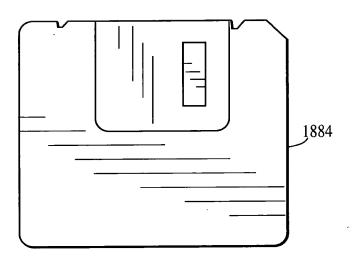


FIG. 18